

Estimates

Max Performance

Implementation	Performance (kbs)	Power (mW)	Norm	Area (mm ²)	Effort (days)
Reference	100.00	N/A	N/A	N/A	N/A
ASIC	58000.00	0.015	1.0	2.50	180.00
CP	750.00	191.000	12733.3	3.10	12.00
DSP	582.00	158.000	10533.3	144.00	5.00
ARM	100.00	400.000	26666.7	30.00	4.00

Normalized

Implementation	Performance (kbs)	Power (mW)	Norm	Area (mm ²)	Effort (days)
ASIC	100.00	0.00003	1.0	2.50	180.00
CP	100.00	2.60779	100834.4	3.10	12.00
DSP	100.00	2.77993	107490.7	144.00	5.00
ARM	100.00	400.00000	15466666.7	30.00	4.00

Actual

Max Performance

Implementation	Performance (kbs)	Power (mW)	Norm	Gates	Area (mm ²)	Gates / Area	Power (uW) / Gate	Effort (days)
ASIC	3750.00	50.60	1.0	35100	4.00	8775.00	1.44	30
CP 3	966.00	191.00	3.8	26480	6.69	3958.15	7.21	6
CP 2	793.00	191.00	3.8	47098	6.69	7040.06	4.06	6
DSP	464.70	89.46	1.8	47098	10.24	4599.41	1.90	3
CP 1	118.00	48.00	0.9	50000	2.10	23809.52	0.96	6
ARM	116.48	42.94	0.8	50000	7.47	6695.68	0.86	4
Reference	100.00	N/A	N/A	N/A	N/A	N/A	N/A	N/A

Normalized (100kbs)

Implementation	Performance (kbs)	Power (mW)	Norm	Gates	Area (mm ²)	Gates / Area	Power (uW) / Gate	Effort (days)
ASIC	100.00	0.14	1.0	35100	4.00	8775.00	0.004	30
DSP	100.00	1.97	14.3	47098	10.24	4599.41	0.042	3
CP 3	100.00	2.02	14.7	26480	6.69	3958.15	0.076	6
CP 2	100.00	2.47	17.9	47098	6.69	7040.06	0.052	6
ARM	100.00	36.86	266.8	50000	7.47	6695.68	0.737	4
CP 1	100.00	40.68	294.4	50000	2.10	23809.52	0.814	6

Normalized (1Mbs)

Implementation	Performance (kbs)	Power (mW)	Norm	Gates	Area (mm ²)	Gates / Area	Power (uW) / Gate	Effort (days)
ASIC	1000.00	1.38	1.0	35100	4.00	8775.00	0.039	30
CP 3	1000.00	197.72	143.1	26480	6.69	3958.15	7.467	6